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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

First-Named Inventor: DE JONG, Franciscus
Gerardus Maria
Application No.: 10/067206 Conf.: 7031
Date Filed: 02/05/2002
Notice of Allowance Date: 03/12/2004

Docket No.: NL01 0324
Art Unit: 2829
Examiner: Tang, Minh Nhut
Customer No.: 24738

Title: TEST CIRCUIT AND TEST METHOD FOR PROTECTING AN IC AGAINST
DAMAGE FROM ACTIVATION OF TOO MANY CURRENT DRAWING CIRCUITS
AT ONE TIME

Box ISSUE FEE
Assistant Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

LETTER TO OFFICIAL DRAFTSMAN

Sir:

Enclosed please find two (2) sheets of corrected/substitute drawings for entry in the
above-referenced patent application.

Please charge any fees which may be required, except the issue fee, or credit any
overpayment to Deposit Account No. 14-1270.

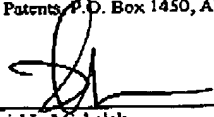
Date: 6/10/04

Respectfully submitted,

By 

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(Date) <u>10-June-2004</u>	(Signature)  (Name) Daniel L. Michalek



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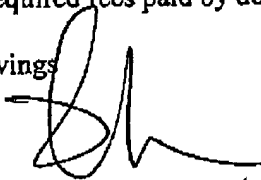
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- ✓ Issue Fee Transmittal – ALL required fees paid by deposit account
- ✓ Letter to Official Draftsman
- ✓ Two (2) sheets of Formal Drawings


(Signature)

Daniel L. Michalek

(Typed or printed name of person signing Certificate)

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